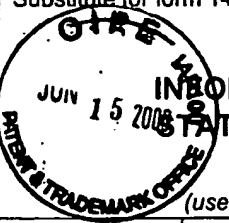
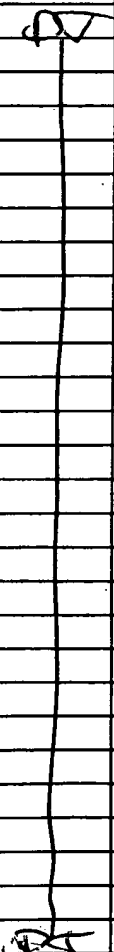



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Substitute for form 1449/PTO			Complete if Known		
			Application Number	09/747,759	
			Filing Date	December 21, 2000	
			First Named Inventor	Thomas A. Kean	
			Group Art Unit	2134	
			Examiner Name	David Yiuk Jung	
Sheet	1	of	3	Attorney Docket Number	13271.5

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publications of Cited Documents MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ²			
	1.	4,120,030		Johnstone	10/10/1978	
	2.	4,465,901		Best	08/14/1984	
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	4.	4,562,305		Gaffney	12/31/1985	
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	27.	6,141,756		Bright	10/31/2000	
	28.	6,151,677		Walter, et al.	11/2000	

Examiner Signature		Date Considered	1/24/07
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				Filing Date	December 21, 2000
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Sheet	2	of	3	Attorney Docket Number	13271.5

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		Number	Kind Code ²			
DJ	29.	6,198,303		Rangasayee	03/06/2001	
	30.	6,212,639		Erickson	04/03/2001	
	31.	6,049,222		Lawman	04/2000	
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Examiner Initials	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publications of Cited Documents MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵				
DJ	44.	WO	98/58305	A1	Deas	12/23/1998		
	45.	WO	99/46774	A1	Kelem	09/16/1999		
	46.	WO	00/49717	A2	Trimberger	08/24/2000		

Examiner Signature	David Jung	Date Considered	1/24/07
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		Examiner Name	David Yiuk Jung
Sheet	3	of	3
		Attorney Docket Number	13271.5

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DJ	47.	XILINX INC., "Xilinx User Guide and Tutorials", 1991, Section "The Best of Xcell", pg 602, column headed "Design Security".	
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	49.	OLDFIELD, John V., et al., "Field Programmable Gate Arrays", Wiley-Interscience, 1995, Chapter 3, pgs. 53-102.	
	50.	ACTEL CORP., "Protecting your Intellectual Property from the Pirates", presentation at DesignCon '98, 1998.	
	51.	ATMEL CORP., Application Note "Programming Specification for Atmel's AT17 and AT17A series FPGA Configuration EEPROMs", 1999.	
	52.	TRISCEND INC., "TRISCEND E5 Configurable Processor Family", Product Description (Preview), July 1999, Section "System Initialisation", pgs. 39-43.	
	53.	ACTEL CORP., "60RS Family SPGA's", Advanced Data Sheet, November 19, 1999.	
	54.	XILINX INC., "Virtex 2.5V Field Programmable Gate Arrays", Advanced Product Specification, 1999, pgs. 1-12.	
	55.	TRISCEND INC., "Implementing Secure Remote Updates Using Triscend E5 Configurable System-on-Chip Devices", Application Note AN-02, January 2000, pgs. 1-16.	
	56.	INTEL CORP., "Intel Processor Identification and the CPU-ID Instruction", Application Note AP-485, Intel Corporation, November 2000.	

Examiner Signature	David Jung	Date Considered	1/24/07
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